



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): James John Casto et al.

Title: INTEGRATED CIRCUIT PACKAGE INCORPORATING
PROGRAMMABLE ELEMENTS

Application No.: 09/484,311

Filed: January 18, 2000

Examiner: E. Lee

Group Art Unit: 2815

Atty. Docket No.: 1001-0087

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March 19, 2002

COMMISSIONER FOR PATENTS
Washington, DC 20231

RESPONSE TO NON-FINAL OFFICE ACTION

This paper is responsive to a Non-Final Office action dated December 19, 2001, having a shortened statutory period for response set to expire March 19, 2002.

AMENDMENT

In the Drawings

A proposed drawing change is being submitted herewith to identify Fig. 1 as prior art. With the Examiner's approval, that change will be incorporated into formal drawings in due course.

In the Specification

Please replace the paragraph beginning on page 4, line 4, with the following paragraph:

Still another embodiment provides a method for setting a parameter value for an integrated circuit. The method includes selectively programming one or more programmable elements located on an integrated circuit package, to selectively couple an internal package node to a supply voltage node. The one or more programmable elements are selectively programmed according to a desired value of an integrated circuit parameter. The parameter may, e.g., be a

B1